



IN THE  
UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANTS: Nicholas J. Kelsey, et al.  
SERIAL NO.: 09/748,098  
FILING DATE: December 21, 2000  
TITLE: System and Method for Instruction Level Multithreading in an Embedded Processor Using Zero-Time Context Switching  
EXAMINER: David J. Huisman  
GROUP ART UNIT: 2183  
ATTY. DKT. NO.: 20880-05093

**CERTIFICATE OF MAILING**

I hereby certify that this correspondence is being deposited with the United States Postal Service in an envelope addressed to: Mail Stop AF, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on the date shown below.

Dated: 6/13/06

By: [Signature]

Hector J. Ribera, Reg. No. 54,397

MAIL STOP AF  
COMMISSIONER FOR PATENTS  
P.O. BOX 1450  
ALEXANDRIA, VA 22313-1450

**AMENDMENT F AND RESPONSE UNDER 37 C.F.R. § 1.116**

Sir:

This amendment for the patent application identified above is in response to the Office Action dated April 13, 2006, which set a shortened statutory period for response that expires on July 16, 2006.

Amendments to the Claims begin on page 2 of this paper.

Remarks begin on page 13 of this paper.

Kindly amend this application as indicated herein.

Do Not Enter  
DOH 6-23-06